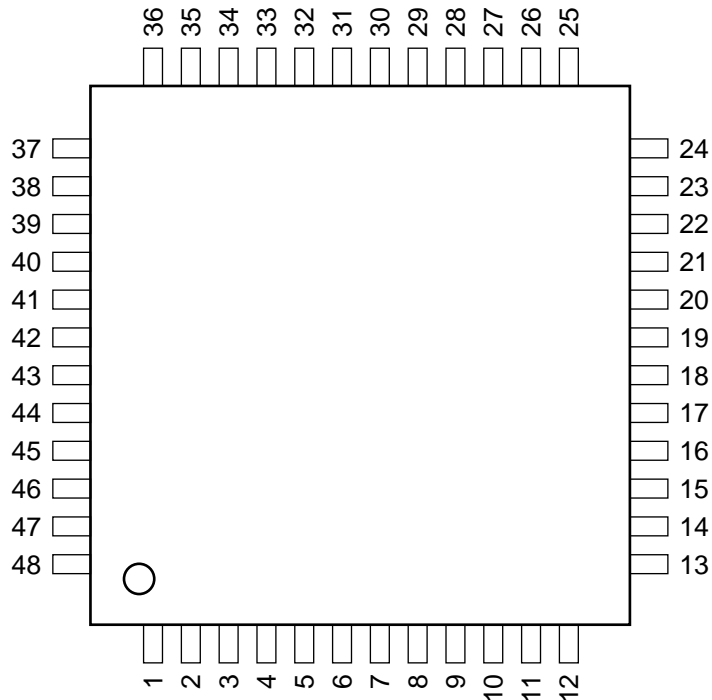


10-BIT SIGNAL PROCESSOR FOR CCD CAMERAS

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	TEST MODE	13	—	DGND	25	O	CCDBYP2	37	O	CMLEVEL
2	O	D0	14	—	DSUBST	26	I	DIN	38	O	VTRBYP
3	O	D1	15	—	DGND	27	I	PIN	39	I	MODE2
4	O	D2	16	I	ADCCLK	28	O	CCDBYP1	40	I	MODE1
5	O	D3	17	—	DVCC	29	I	PGACONT1	41	I	PB REC
6	O	D4	18	I	STBY	30	I	PGACONT2	42	I	CAM VIDEO
7	O	D5	19	I	PBLK	31	—	AGND	43	—	AVCC
8	O	D6	20	I	CLPOB	32	O	CLAMP BIAS	44	I	FLP
9	O	D7	21	I	SHP	33	—	AVCC	45	—	AGND
10	O	D8	22	I	SHD	34	I	LINE	46	—	SUBST
11	O	D9	23	I	CLPDM	35	I	RECONT	47	O	VRB
12	—	DVCC	24	I	FMC	36	I	CHROMA	48	O	VRT

INPUTS

ADCCLK	: ADC SAMPLE CLOCK
CAM VIDEO	: MODE CONTROL
CHROMA	: CHROMA
CLPDM	: CAMERA MODE CLAMP DUMMY
CLPOB	: CAMERA MODE CLAMP BLACK LEVEL
DIN	: CDS
FLP	: CDS FLIP CONTROL (INTERNAL PULL DOWN)
FMC	: FLICKER MODE CONTROL (ACTIVE HIGH, INTERNAL PULL-DOWN)
LINE	: LINE
MODE1, MODE2	: ADC TEST MODE CONTROL
PB REC	: MODE CONTROL
PBLK	: PIXCL BLANKING
PGACONT1	: CAMERA MODE AGC COARSE GAIN CONTROL
PGACONT2	: CAMERA MODE AGC TINE GAIN CONTROL
PIN	: CDS
RECONT	: VTR GAIN CONTROL
SHD	: CAMERA MODE DATA CLOCK
SHP	: CAMERA MODE REFERENCE CLOCK
STBY	: SET HIGH FOR POWER DOWN MODE (INTERNAL PULL DOWN)
TEST MODE	: RESERVED PIN WHICH USER SHOULD NOT CONNECT

OUTPUTS

CCDBYP1, CCDBYP2	: CCD BYPASS
CLAMP BIAS	: LINE CLAMP BYPASS
CMLEVEL	: COMMON MODE LEVEL BYPASS
D0 - D9	: DATA
VRB	: BOTTOM REFERENCE BYPASS
VRT	: TOP REFERENCE BYPASS
VTRBYP	: VTR AGC BYPASS

